

### **ABSTRACT OF THE DISCLOSURE**

A semiconductor memory includes a plurality of memory arrays and a plurality of sense amplifiers arranged alternately with respect to each of the memory arrays. The sense amplifiers are coupled to bit lines of the memory arrays to form an open bit line type semiconductor memory. In conjunction with this structure, two memory arrays at end portions of the semiconductor memory device are both selected together, whereas a memory array in an inner portion of the semiconductor memory device is selected independently.